







Lessons learned, the way ahead, and governance



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- Why: community effort required to address future technological and HPC challenges
- Vision: establish national ESM capability and support team that will help to save resources, create synergies, share insights, and disseminate knowledge



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- No agreement on core components
- Collectively agreed upon technical criteria
- Workshops crucial element of strategy



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- No decision on core components
- Formation of steering committee
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- BMBF project started in Nov 2021



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Focus today: What happened since last workshop?

Initial agile strategy

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Dec 2023: first version of our agile strategy





Agile: we consistently refine our strategic framework





Our strategy can only be further developed through collaboration with you during our workshops, sprint checks, sprints, and working groups.





Focus today:

Four points that shaped our strategy over the past year



- 1. Specify components
- 2. Enhance community engagement
- 3. Refine sprint process
- 4. Sprint allocation and Code of Conduct



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Infrastructure

 Enable the interaction with core and optional components
 Current coupler and interface: YAC and ComIn

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Impact



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Infrastructure

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Optional

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Governance: Institutions



Congruence of institutional and collective goals

- We align our individual and collective objectives to create a harmonious and effective collaborative environment
- Cooperation and dedication of contributing organizations are crucial for natESM
- Formal contracts are impractical; voluntary participation is essential due to evolving priorities of model-developing institutions
- Community usage and organizational commitments are key

Governance: Steering group



Steering group's role is pivotal

- Composition and role endorsed by PT-DLR and BMBF
- Determines natESM direction and strategy, in consultation with community
- Evaluates inclusion of components, based on technical criteria and sprint outcomes
- Lessons learned through sprints are vital
- Unsustainable components will be removed

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Contribute to our GitLab platform





https://nat-esm-system.dkrz.de/

- 1. Promote your model or module
- 2. Share your experiences with other community members

Initiate a working group





Current working groups:

- Communication
 (completed → Website, GitLab, Mattermost)
- 2. Training (running)
- 3. Land-ice component

(running, WG meets this afternoon!)

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Shape the focus of our workshops





natESM workshops:

- 1. Annual community workshop
- 2. Technical training workshops
- 3. Focused workshops (not preplanned; based on your needs)

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Completed 8 sprints (6 since last workshop)



01	ICON-Art	Analysis of ART code for GPU porting	2 months
02	ICON-mHM-YAC	Online coupling mHM into ICON using YAC	6 months
03	FESOM	Port FESOM 2.1 to JUWELS booster and Levante- GPU	6 months
04	ParFlow	Port ParFlow to AMD GPUs, inspection of RAPID Memory Manager and Hipification, performance analysis	6 months
05	MESSy	Optimize data transfers between host (CPU) and device (GPU)	4 months
06	ESMValTool	Updating the remaining non-lazy preprocessor functions to be memory efficient AND updating ESMValCore	6 months
07	НАМОСС	Concurrent HAMOCC on GPU	6 months
08	MESSy-ComIn	Couple MESSy to ICON via ComIn	6 months

Completed even more sprint checks (10)



1	MESSy-ComIn	Couple MESSy to ICON via ComIn	Full sprint
2	PDAF	Porting Kalman filter to GPUs	Plans to apply
3	PALM	Is porting of modules to GPU possible?	Full sprint
4	TSMPv2	Replace OASIS with YAC	Full sprint
5	Jena CSIS	Check if GPU porting is possible	
6	CLEO	Parallelize CLEO using Kokkos	Full sprint
7	MESSy	Solving performance issues	Full sprint
8	PISM	Check if GPU improvement is possible	Plans to apply
9	ISSM	Check if GPU improvment is possible	Plans to apply
10	FABM	ICON-O-FABM interface	Plans to apply
11	netCDF	Define format for data with time-changing resolution	Under review



Exchange with our RSEs proved beneficial, leading to two changes:

1. Sprint check mandatory now

Every full application must be preceded by a sprint check

2. Flexible sprint duration

Sprints now available for flexible durations ranging from 3 to 6 months



Connect with our RSEs today and tomorrow





Seize the opportunity and talk to our RSEs to explore whether a sprint check could be beneficial for you

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Sprint allocation and Code of Conduct



- Sprint allocation is based on a ticket system
- Sprint assignments are decided by natESM leadership team in consultation with RSEs
- Collaboration between sprint applicants and RSEs represents an unusual constellation (no line management)
- RSEs continually improve their skills, enhancing their ability to support our core and infrastructure components
- Please review and internalize our first version of the natESM Code of Conduct, which will be made available in spring



natESM is not just about GPUs and GPU porting

- Misconception: Some think our support is limited to GPU porting
- **Clarification:** GPUs are not always the ultimate solution
 - \rightarrow CPU systems will always be around
 - → Some components and algorithms do not map well onto GPU architecture
- RSEs recommended solution:
 - \rightarrow Exploit heterogeneous systems through CPU-GPU coupling and hybrid approaches
 - → Components running only on CPU systems can still be heavily optimized (Communication, IO, Shared memory, etc.)







- Community engagement strengthened through workshops and sprints
- Introduction of sprint checks
- Community platforms: GitLab, Mattermost
- 1st version of agile strategy in Dec 2023 with decisions on first components
- 1st version of Code of Conduct for natESM ready in spring